

FIG. 1

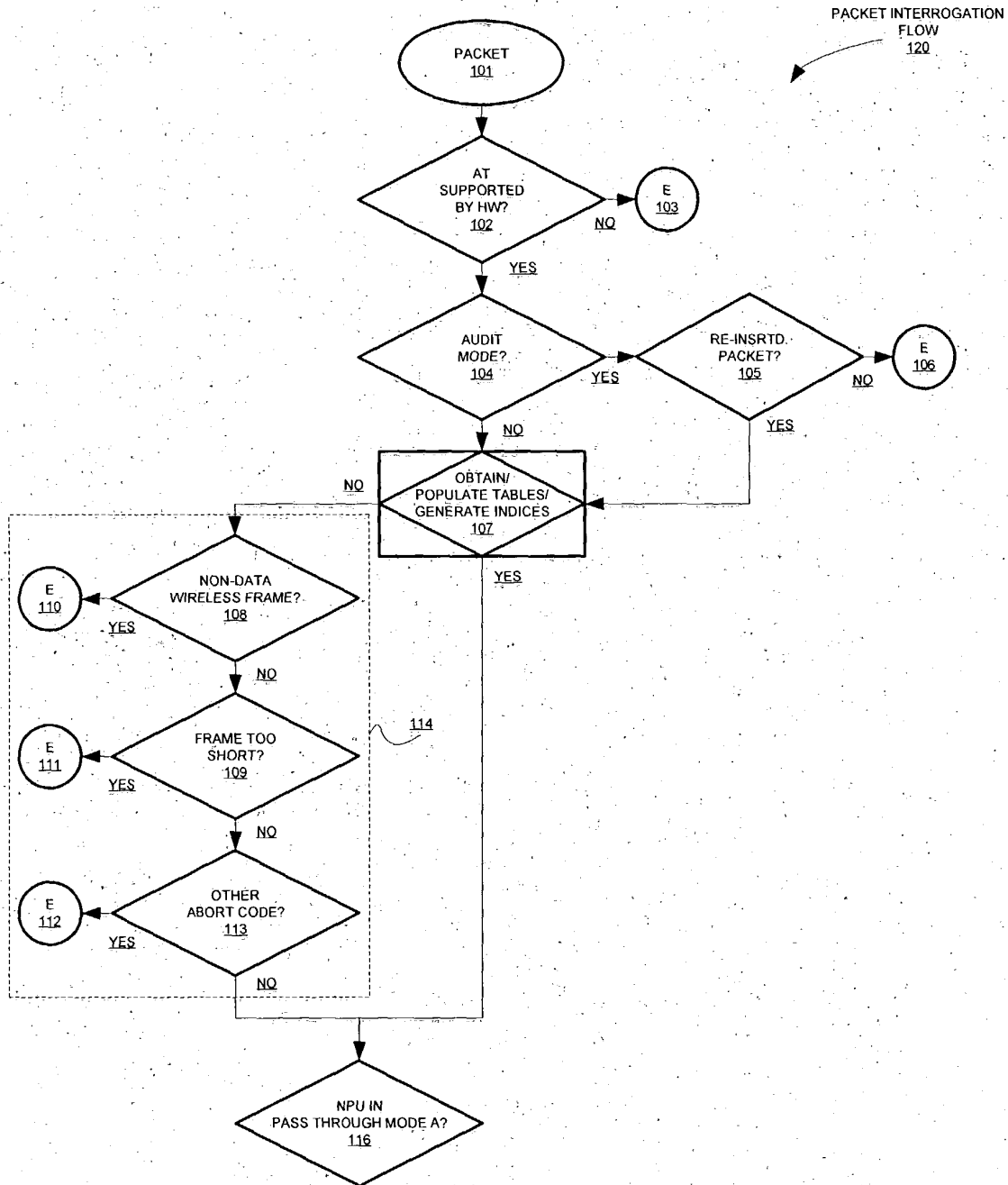


FIG. 2A-1

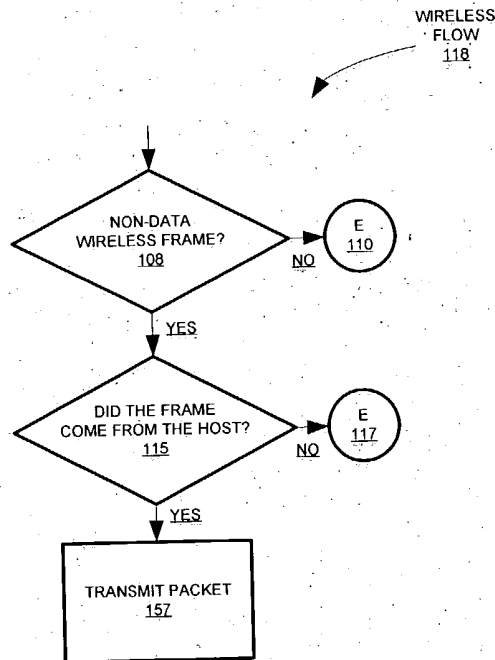


FIG. 2A-2

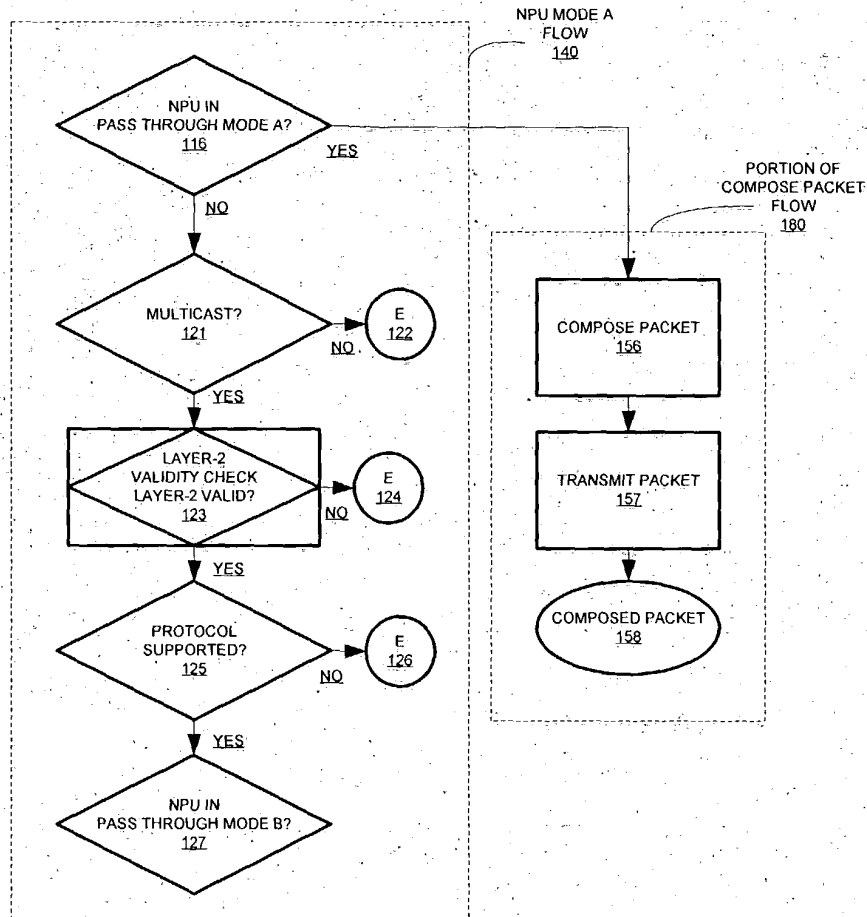


FIG. 2B-1

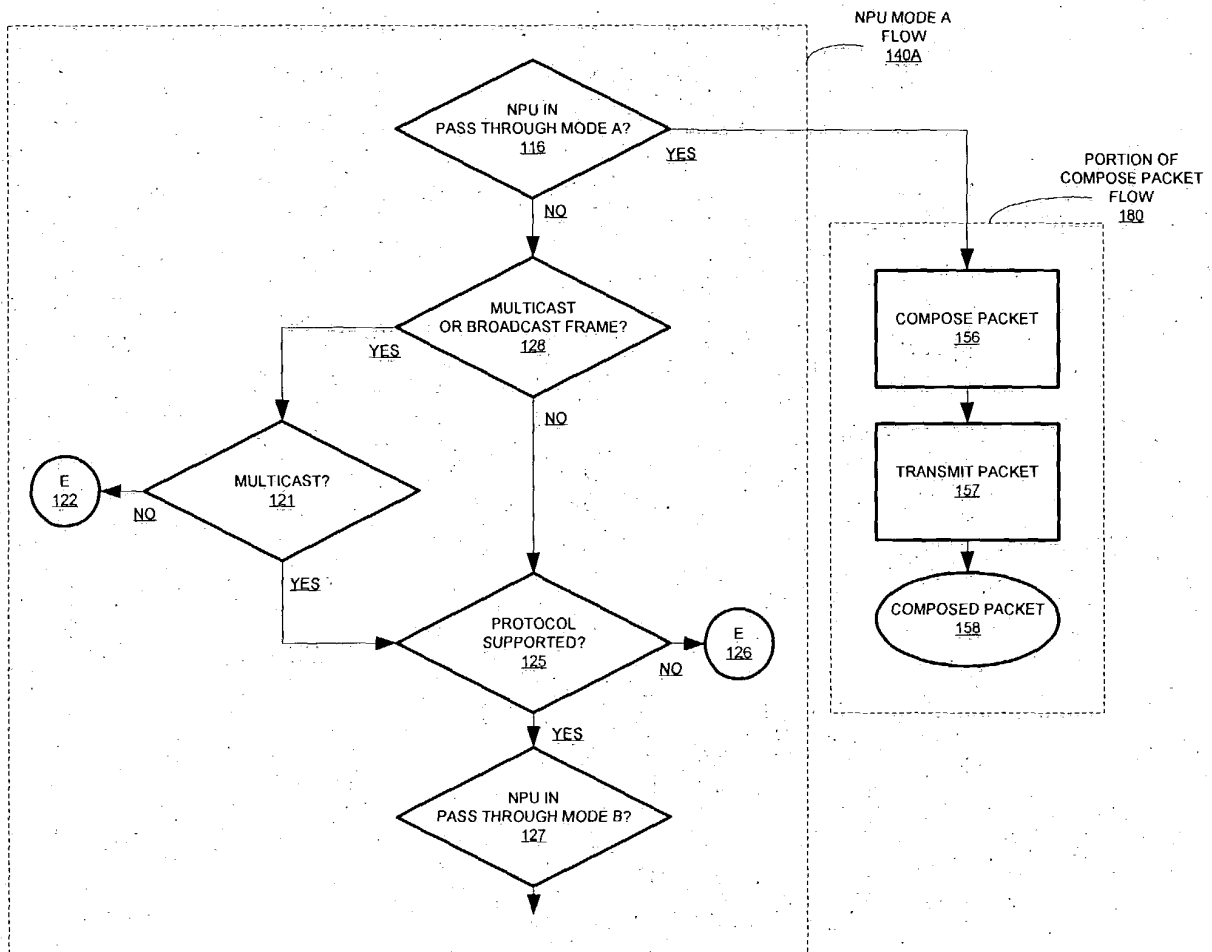


FIG. 2B-2

NPU MODE B  
FLOW  
160

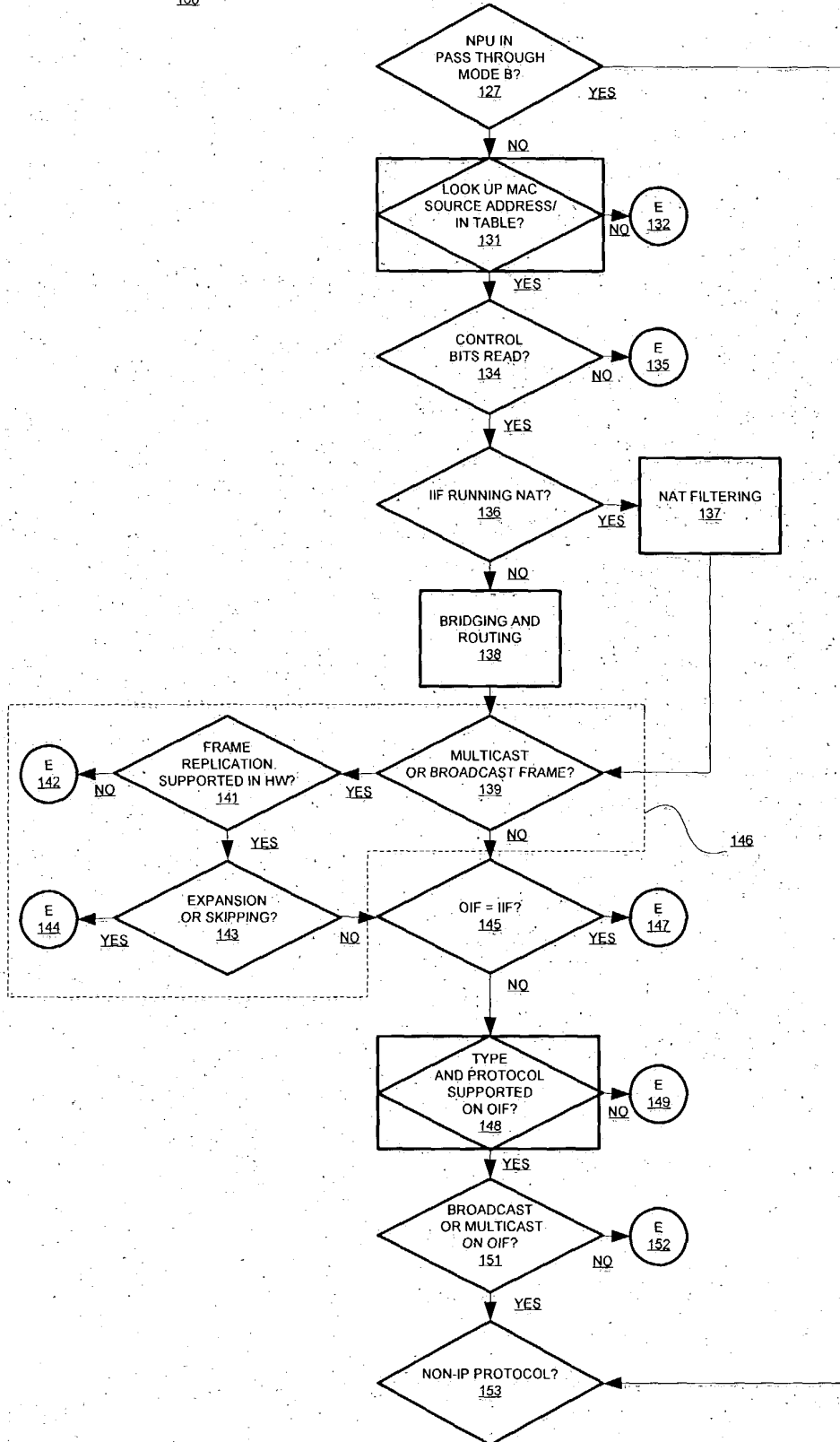


FIG. 2C

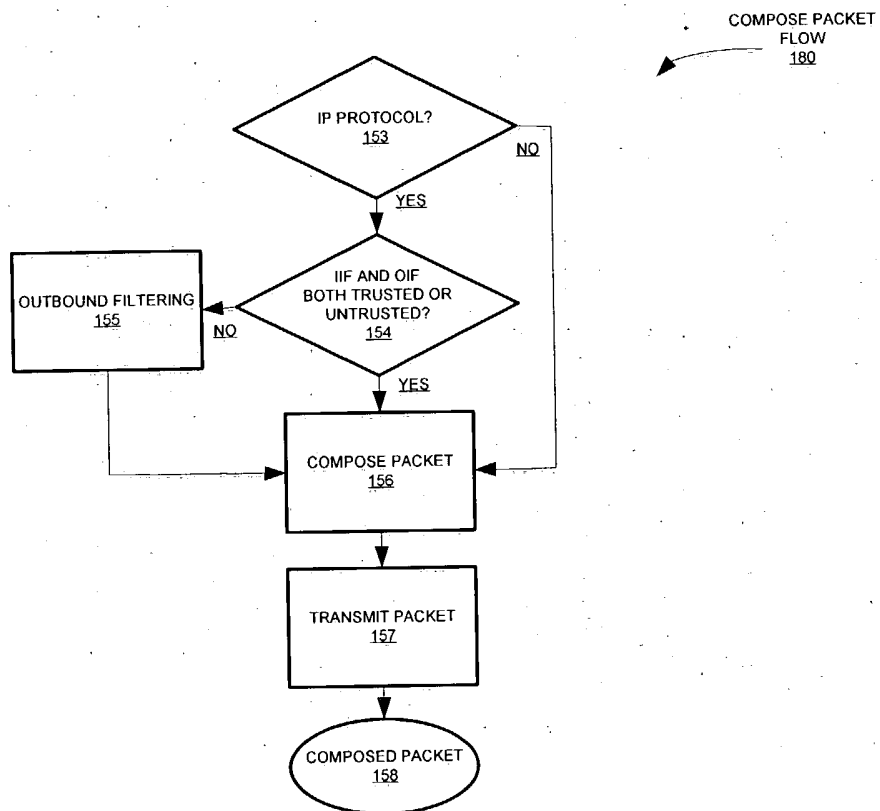


FIG. 2D-1

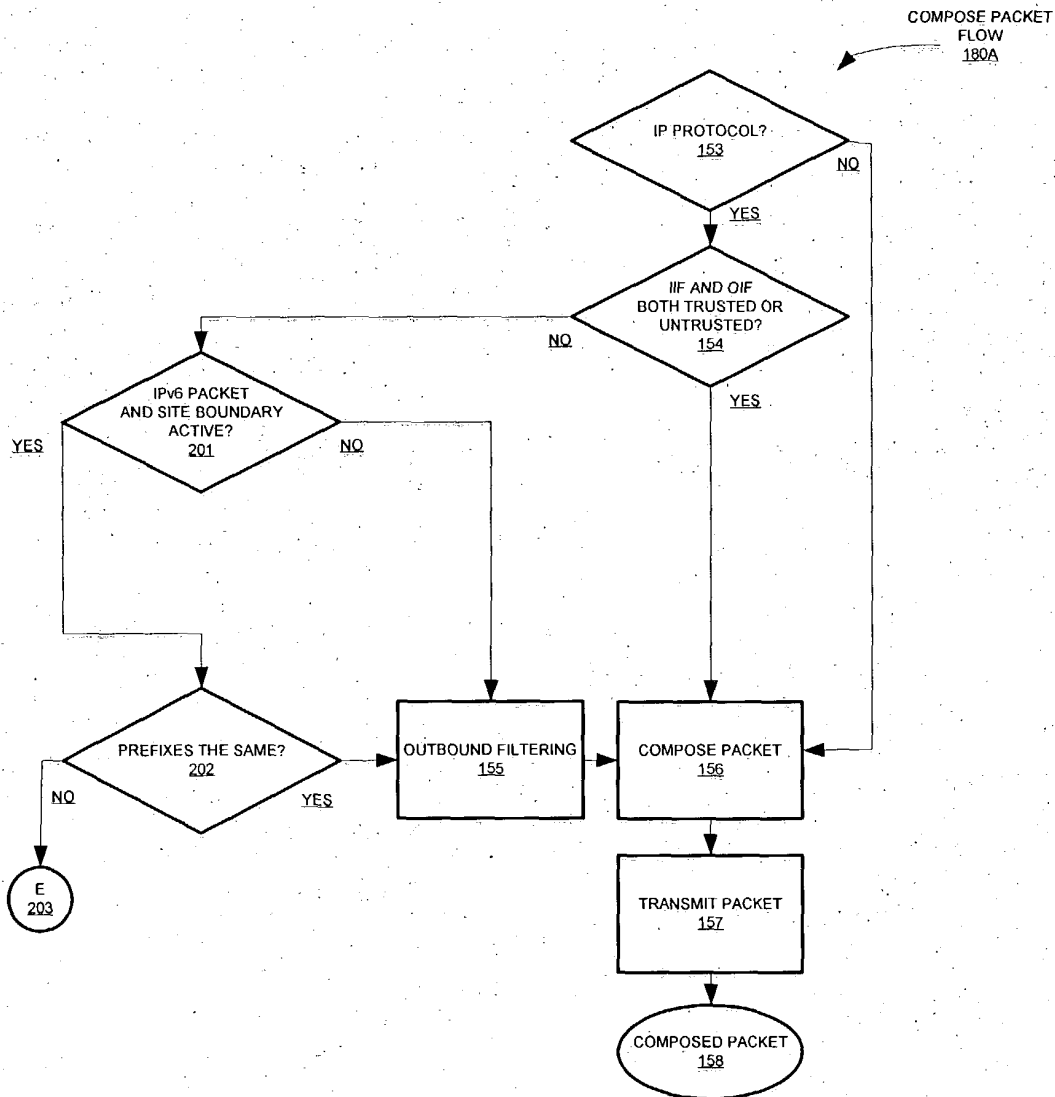


FIG. 2D-2



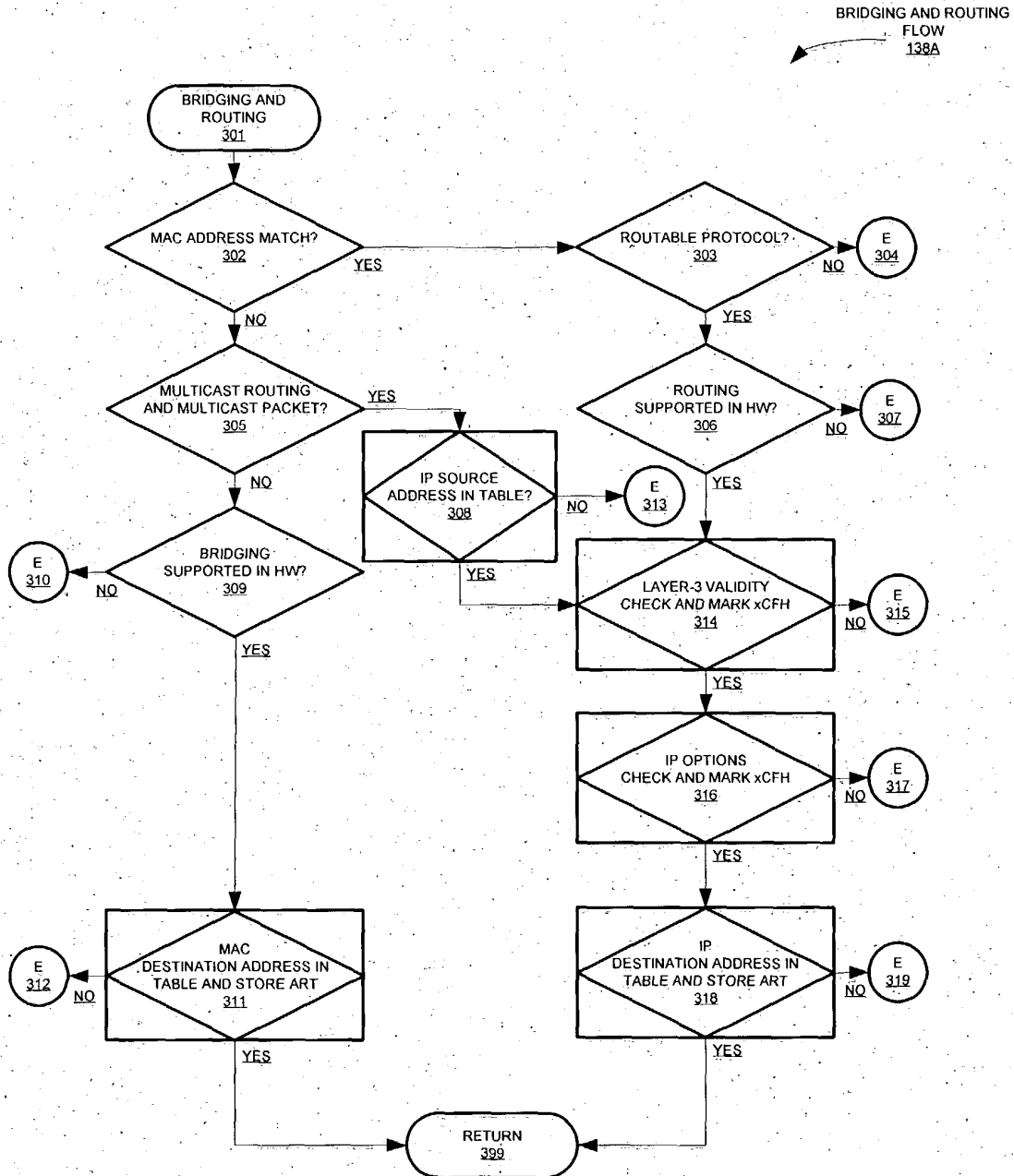


FIG. 3A

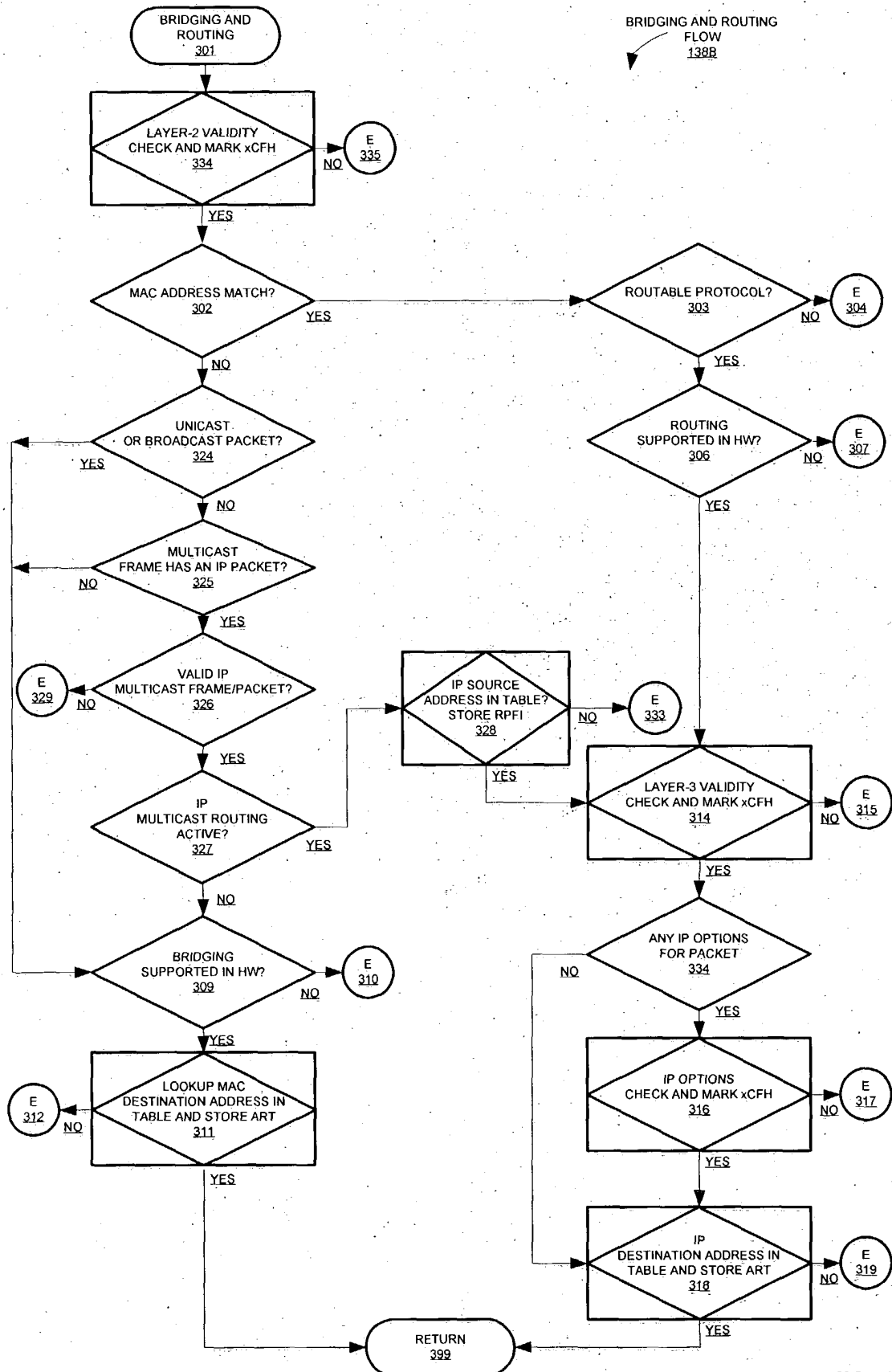


FIG. 3B

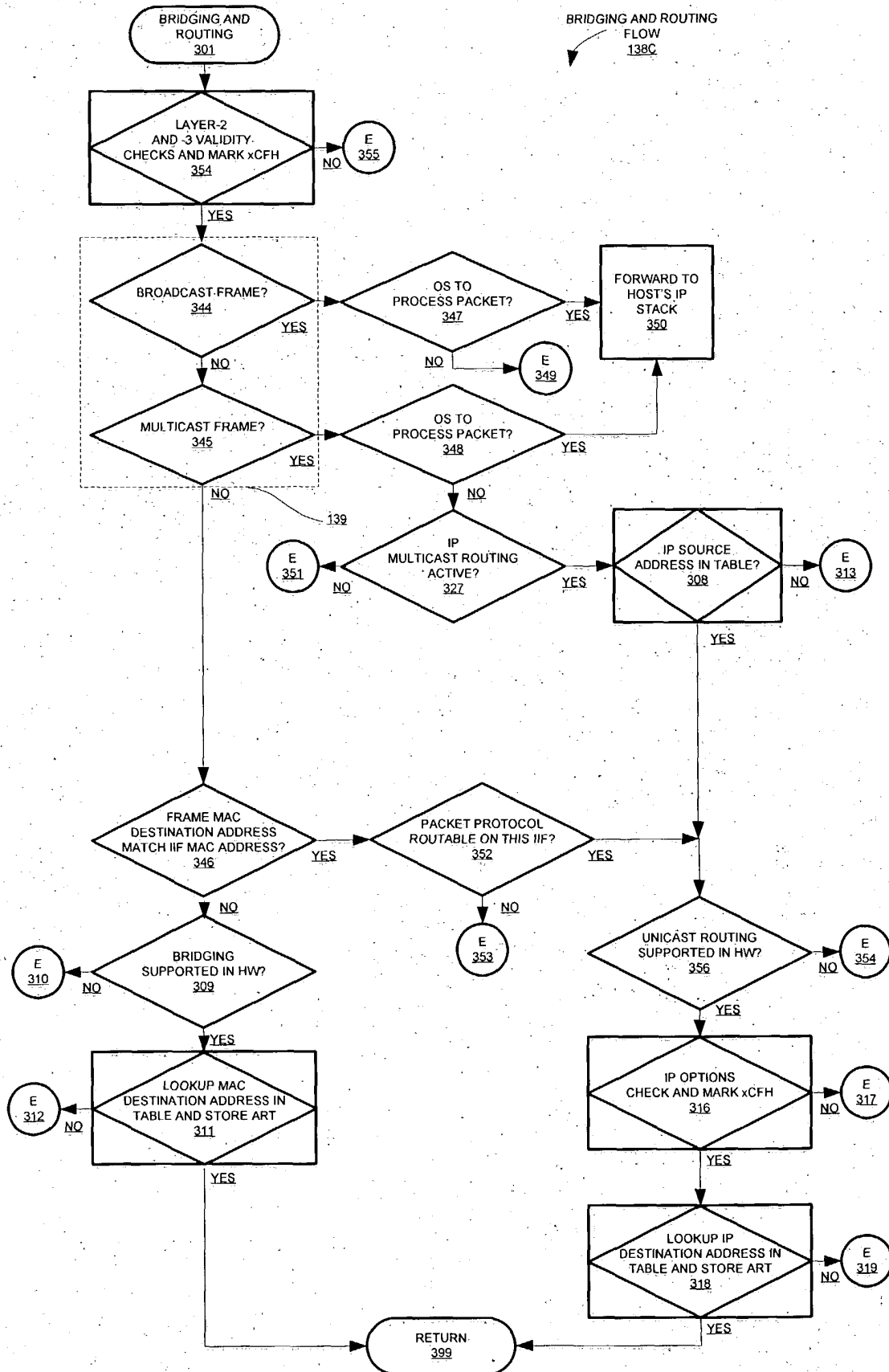


FIG. 3C

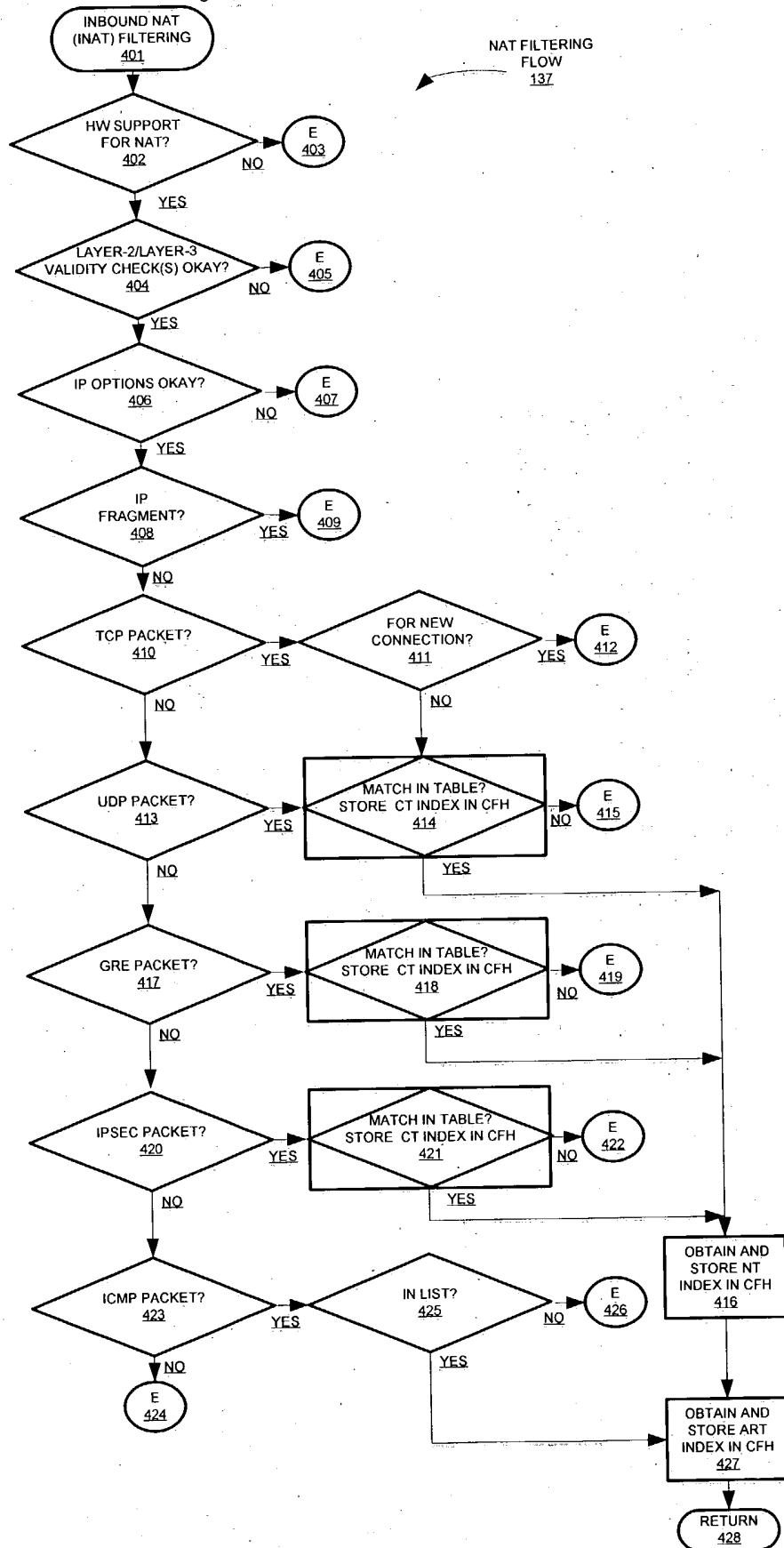


FIG. 4A

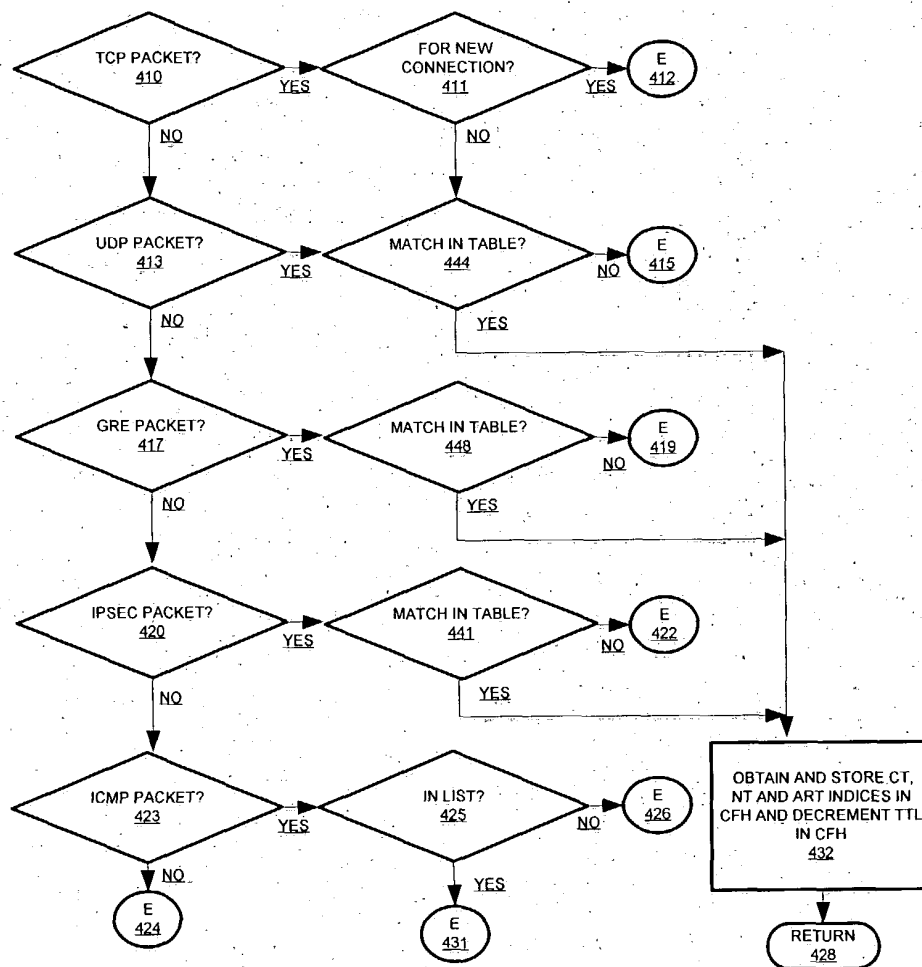


FIG. 4B

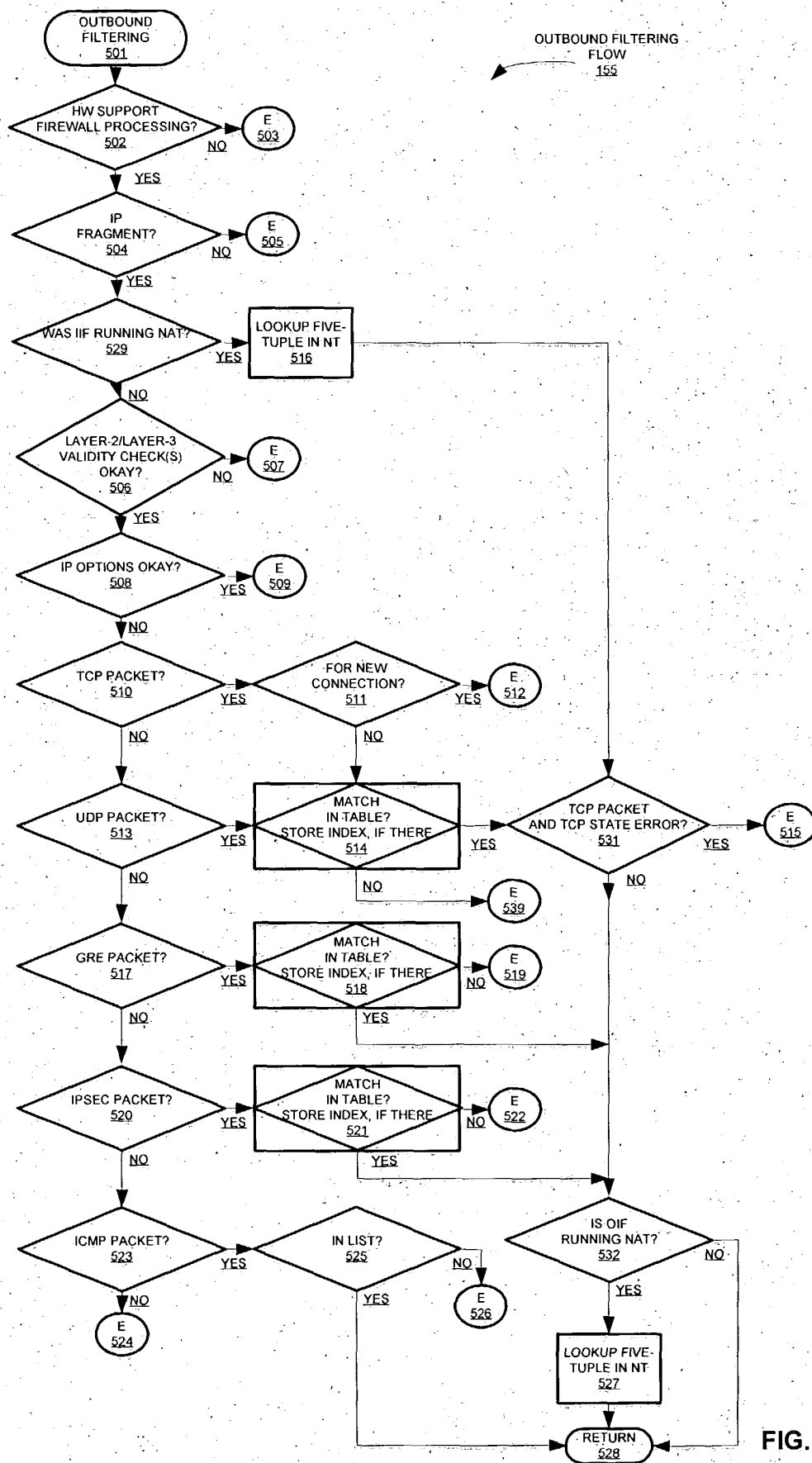
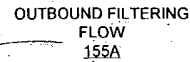


FIG. 5A



**FIG. 5B**

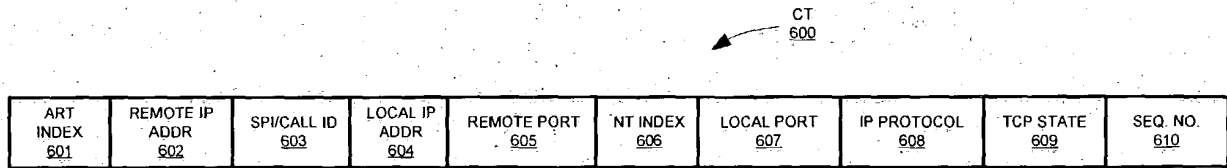


FIG. 6

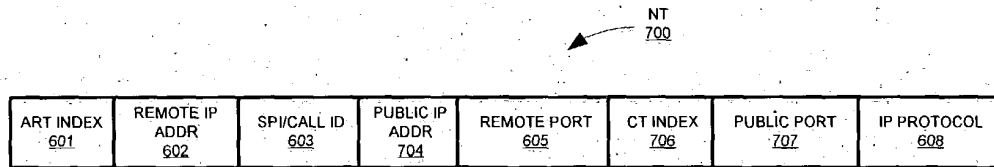


FIG. 7

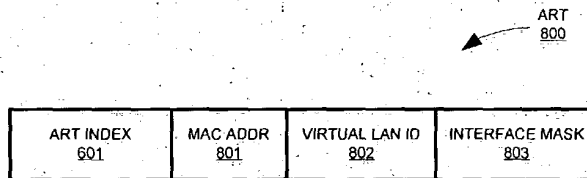


FIG. 8

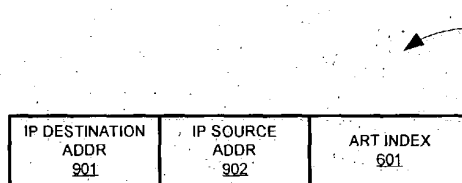


FIG. 9A



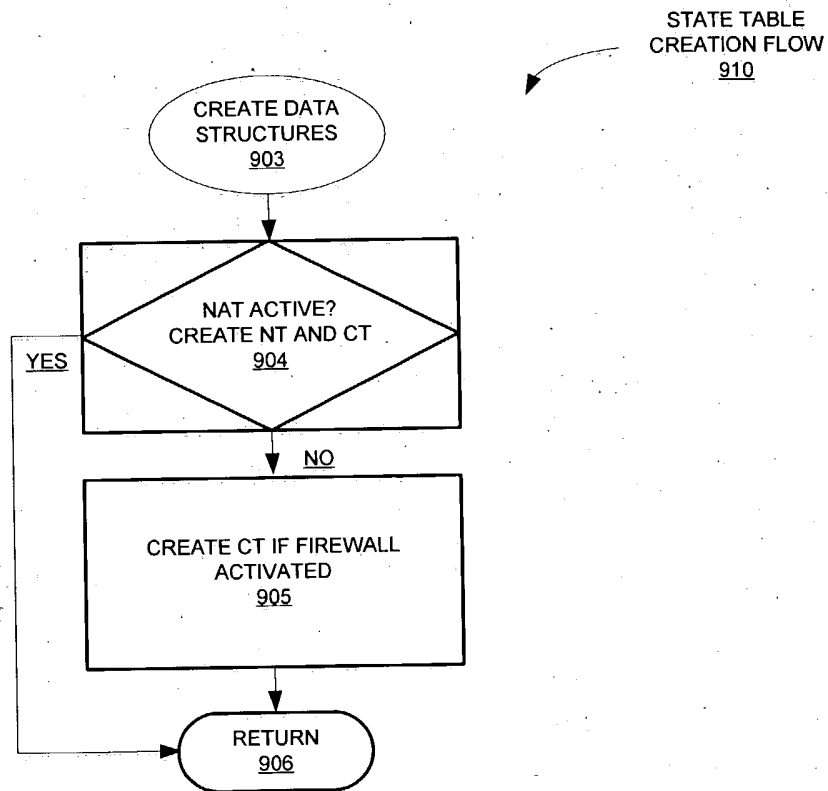
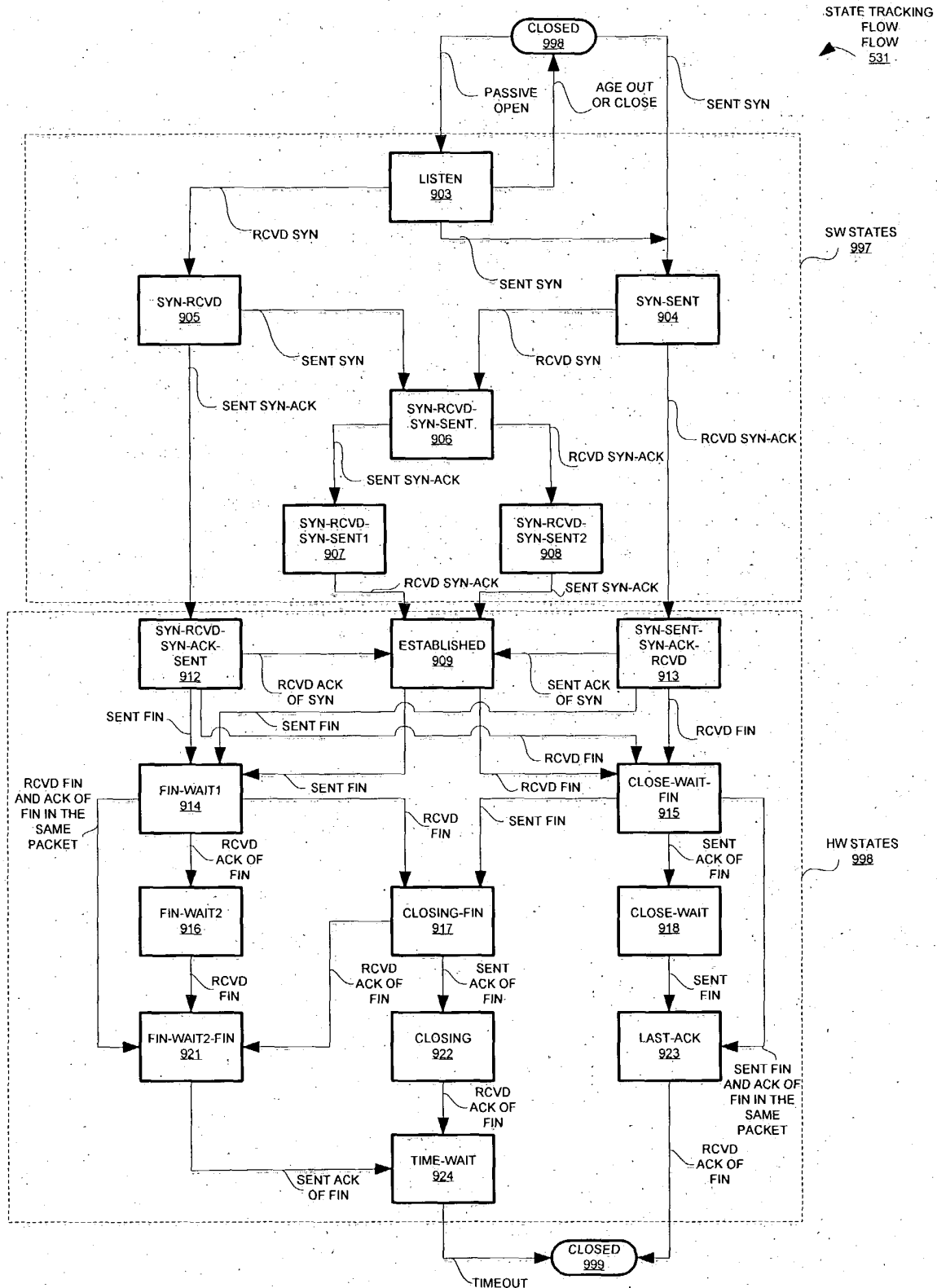


FIG. 9B



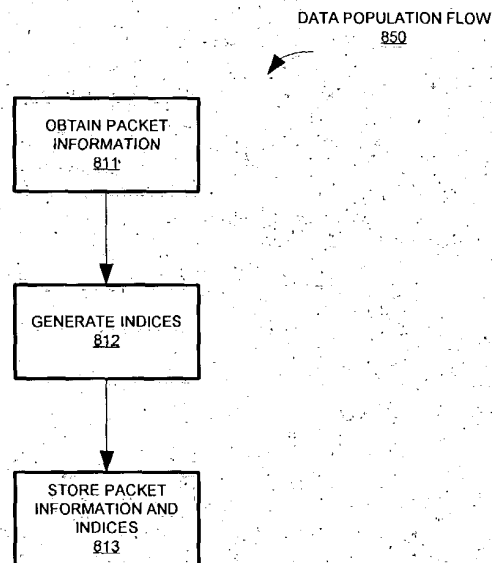


FIG. 11

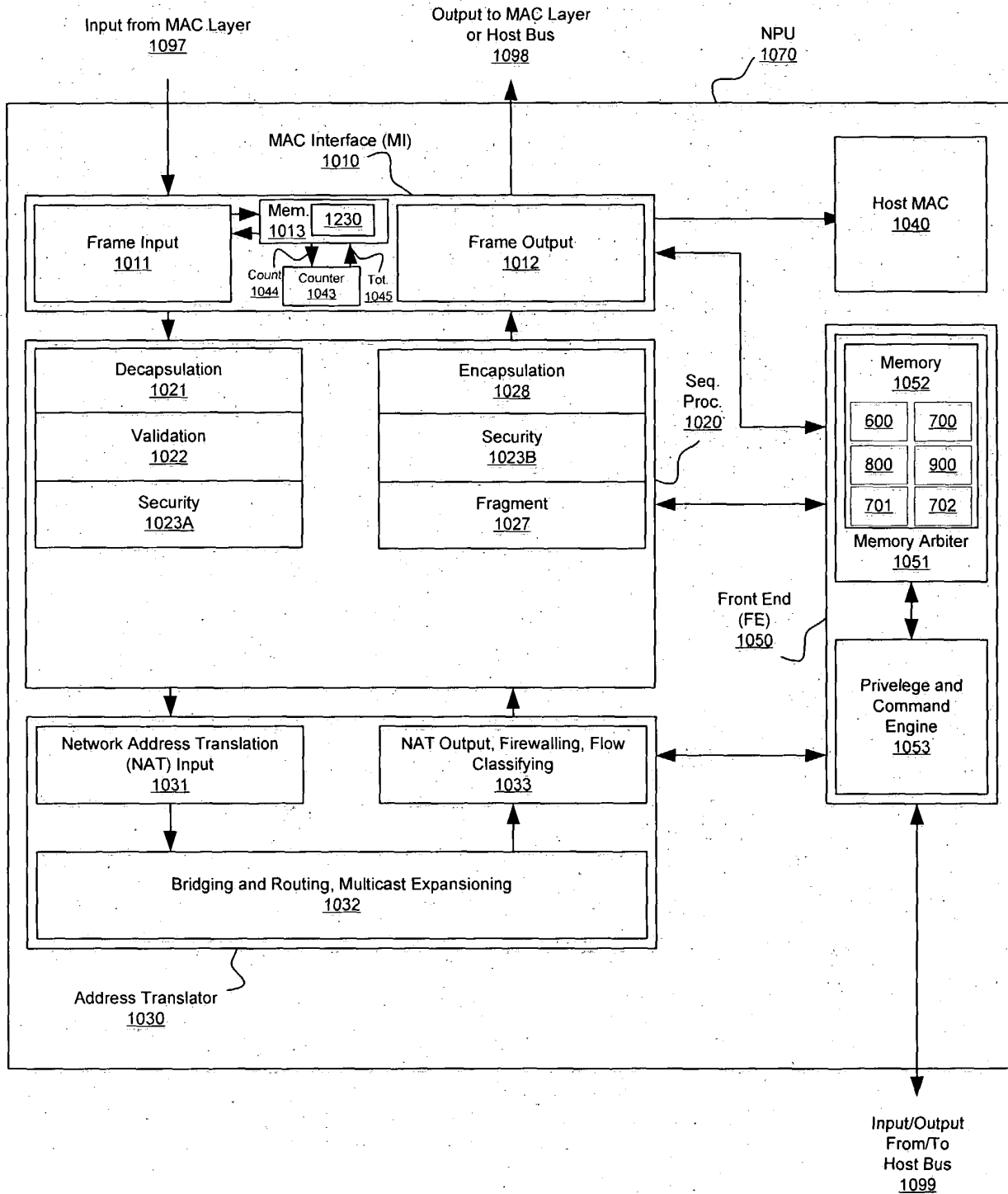
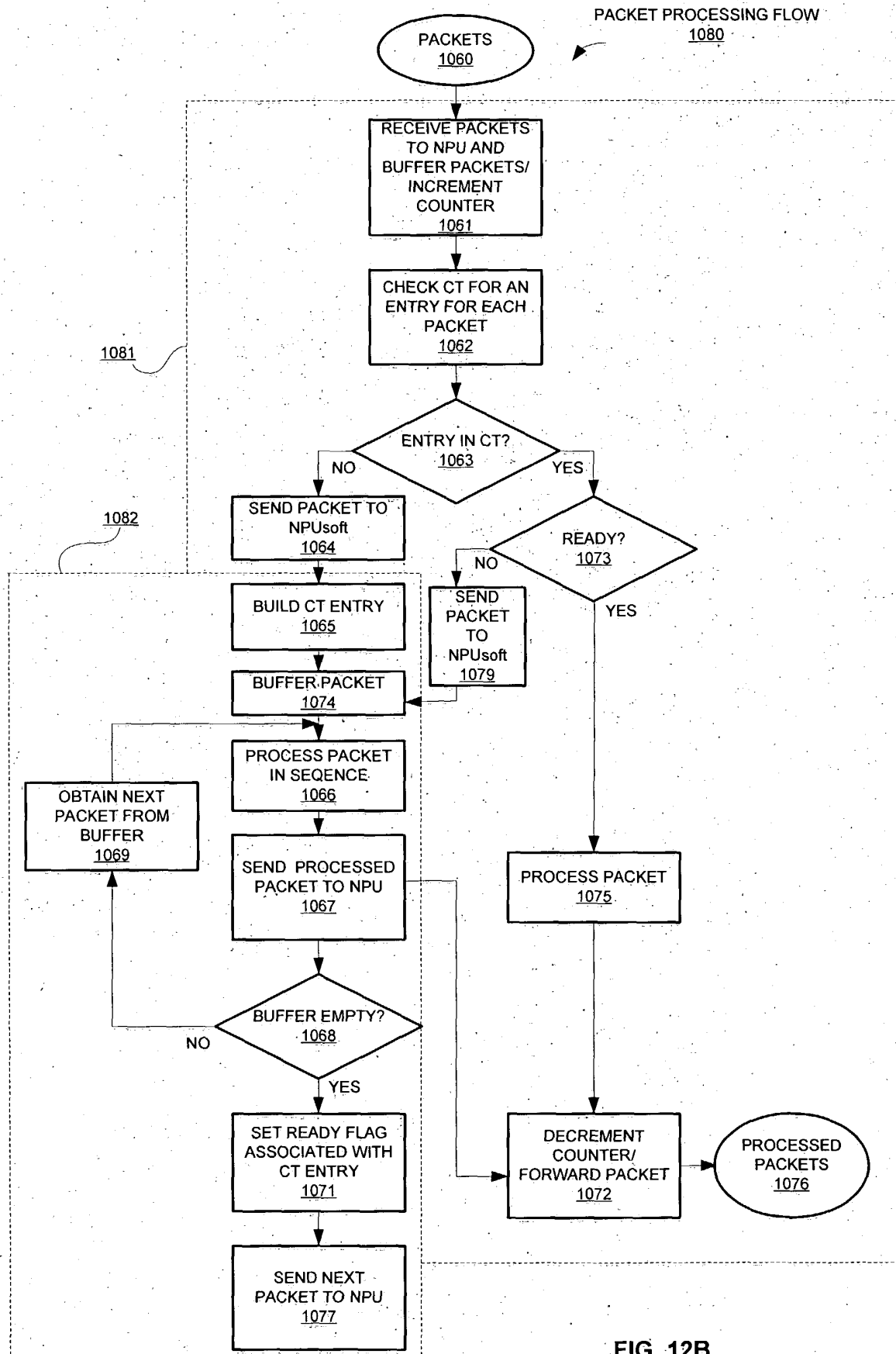


FIG. 12A



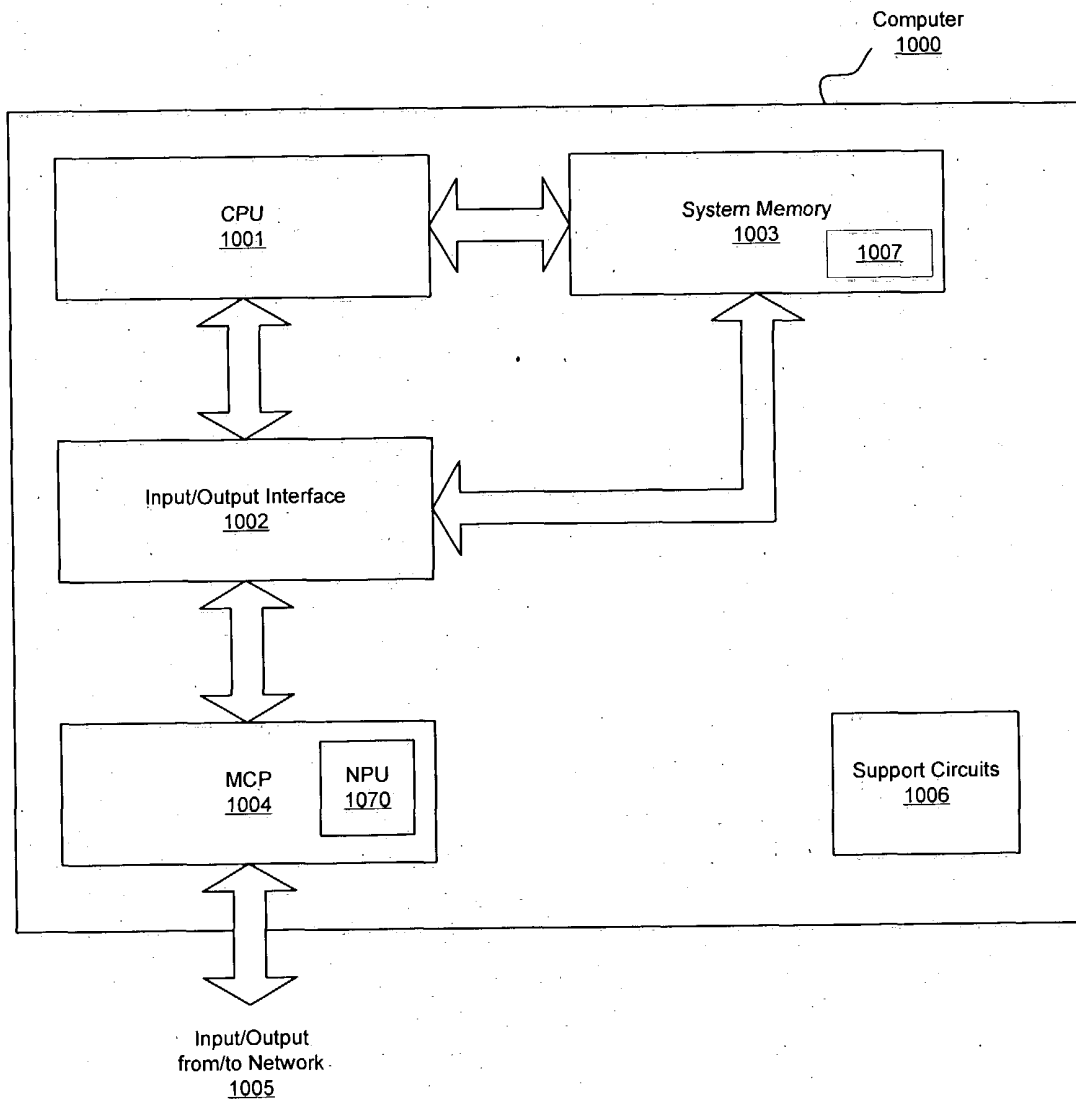


FIG. 13

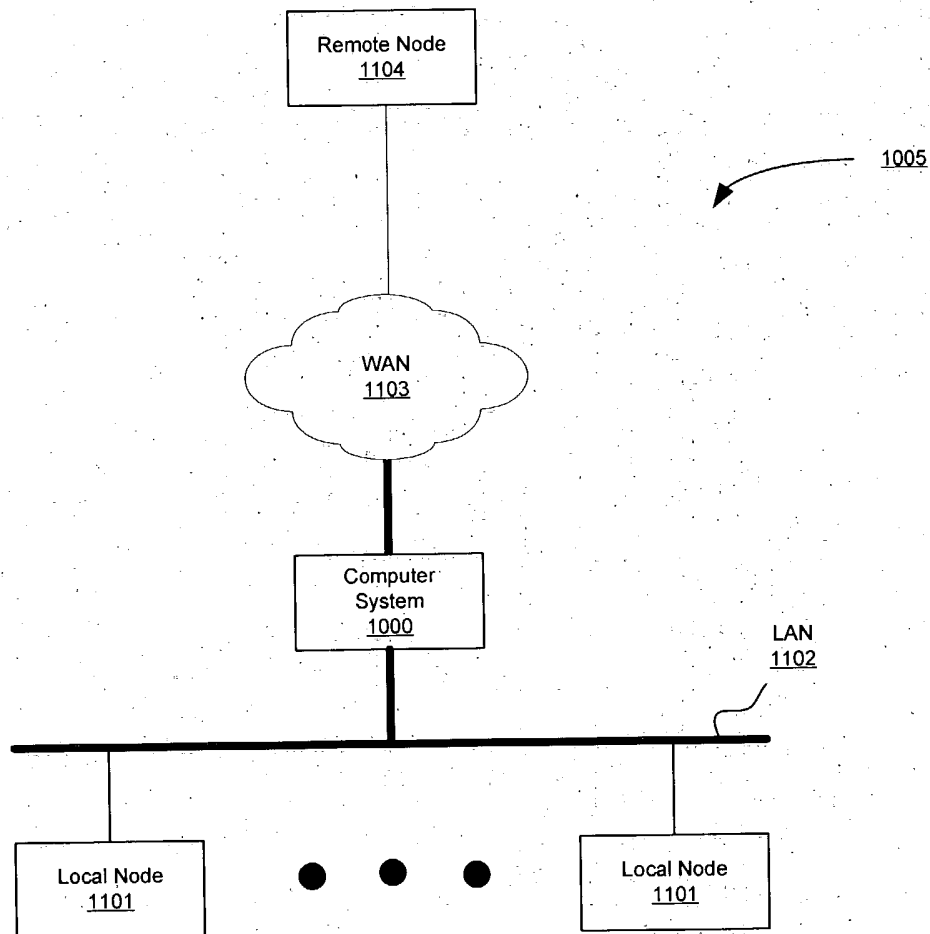


FIG. 14

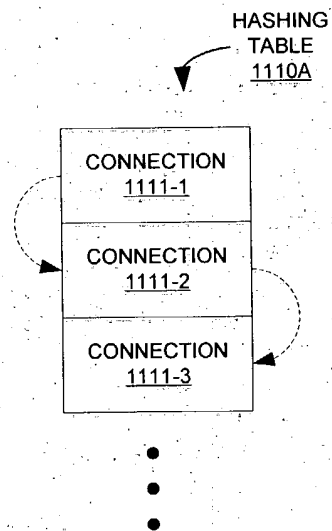


FIG. 15A

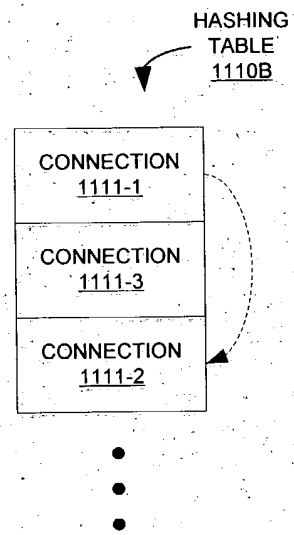


FIG. 15B



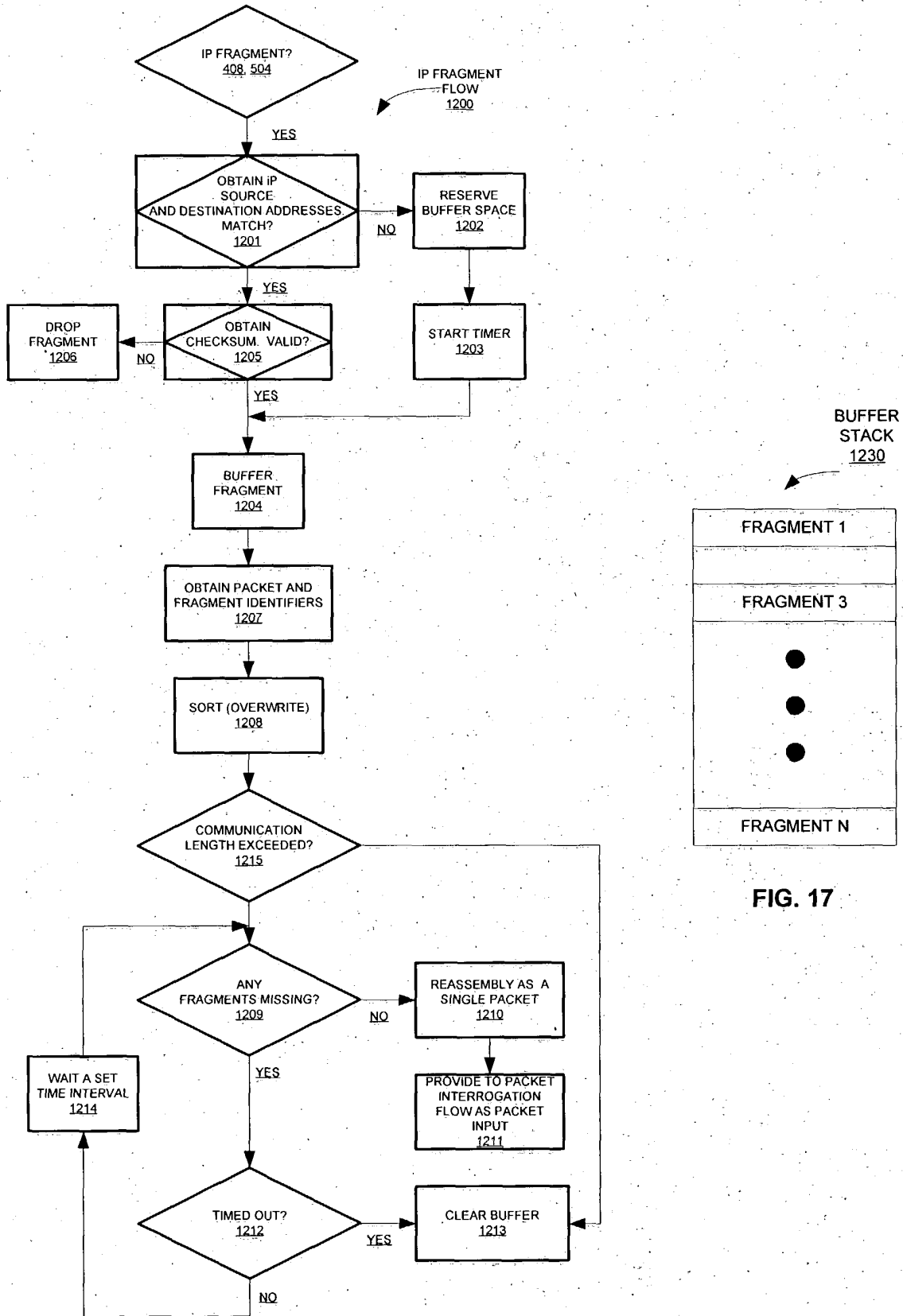


FIG. 17

FIG. 16